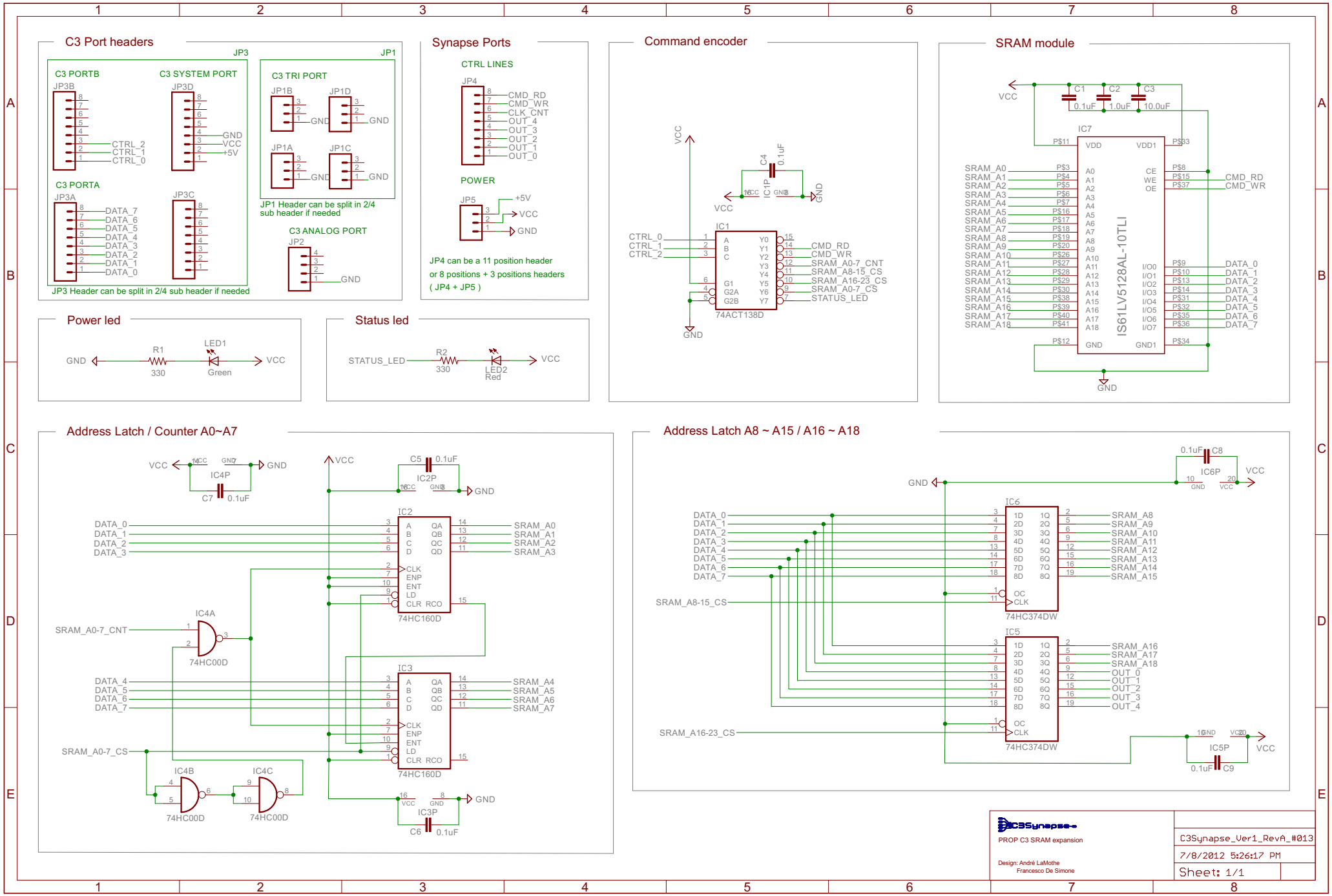
C3 Port headers
JP3
C3 PORTB
JP3B
CTRL_2
CTRL_1
CTRL_0
C3 SYSTEM PORT
JP3D
GND
VCC
+5V
C3 PORTA
JP3A
DATA_7
DATA_6
DATA_5
DATA_4
DATA_3
DATA_2
DATA_1
DATA_0
JP3C
JP3 Header can be split in 2/4 sub header if needed
JP1
C3 TRI PORT
JP1B
JP1D
JP1A
JP1C
JP1 Header can be split in 2/4 sub header if needed
C3 ANALOG PORT
JP2
Synapse Ports
CTRL LINES
JP4
CMD_RD
CMD_WR
CLK_CNT
OUT_4
OUT_3
OUT_2
OUT_1
OUT_0
POWER
JP5
JP4 can be a 11 position header
or 8 positions + 3 positions headers
(JP4 + JP5)
Command encoder
IC1P
C4
0.1uF
IC1
74ACT138D
CTRL_0
CTRL_1
CTRL_2
CMD_RD
CMD_WR
SRAM_A0-7_CNT
SRAM_A8-15_CS
SRAM_A16-23_CS
SRAM_A0-7_CS
STATUS_LED
SRAM module
IC7
IS61LV5128AL-10TLI
C1
C2
C3
0.1uF
1.0uF
10.0uF
SRAM_A0
SRAM_A1
SRAM_A2
SRAM_A3
SRAM_A4
SRAM_A5
SRAM_A6
SRAM_A7
SRAM_A8
SRAM_A9
SRAM_A10
SRAM_A11
SRAM_A12
SRAM_A13
SRAM_A14
SRAM_A15
SRAM_A16
SRAM_A17
SRAM_A18
DATA_0
DATA_1
DATA_2
DATA_3
DATA_4
DATA_5
DATA_6
DATA_7
Power led
R1
330
LED1
Green
Status led
R2
330
LED2
Red
Address Latch / Counter A0~A7
IC4P
C7
0.1uF
C5
IC2P
IC2
74HC160D
IC4A
74HC00D
IC3
IC4B
IC4C
IC3P
C6
Address Latch A8 ~ A15 / A16 ~ A18
C8
IC6P
IC6
74HC374DW
IC5
IC5P
C9
PROP C3 SRAM expansion
Design: André LaMothe
Francesco De Simone
C3Synapse_Ver1_RevA_#013
7/8/2012 5:26:17 PM
Sheet: 1/1